



10/05/2004

17:25

408-4749082

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PAGE 01/08

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: DE LEEUYW, Dagobert  
M.  
Application No.: 09/817107 Conf.: 3801  
Date Filed: 03/26/2001  
Notice of Allowance Date: 07/26/2004

Docket No.: NL000149  
Art Unit: 2822  
Examiner: Rose, Keisha L.

Title: Integrated Circuit Provided With A Substrate And Memory Transponder

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Facsimile: (703) 746-4000

**CERTIFICATE OF TRANSMISSION**  
under 37 CFR §1.8

I hereby certify that this correspondence is being facsimile transmitted to the United States and Trademark Office at facsimile number (703) 746-4000, on October 5, 2004. The documents listed below are included in this transmission:

- ✓ Issue Fee Transmittal – ALL required fees paid by deposit account
- ✓ Letter to Official Draftsman
- ✓ Four (4) sheets of Formal Drawings

  
(Signature)

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Daniel L. Michalek*(Typed or printed name of person signing Certificate)*

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Docket No.: NL000149  
Art Unit: 2822  
Examiner: Rose, Keisha L.  
Customer No.: 24738

Title: INTEGRATED CIRCUIT PROVIDED WITH A SUBSTRATE AND MEMORY  
TRANSPONDER

Box ISSUE FEE  
Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## LETTER TO OFFICIAL DRAFTSMAN

Sir:


Enclosed please find four (4) sheets of corrected/substitute drawings for entry in the  
above-referenced patent application.

Please charge any fees which may be required, except the issue fee, or credit any  
overpayment to Deposit Account No. 14-1270.

Date: 10/05/2004

Respectfully submitted,

By

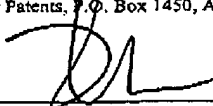
  
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## CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that on the date indicated below this correspondence is being faxed  
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VA 22313-1450".

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10/5/04

(Signature)  
(Name)  
Daniel L. Michalek